

**WHAT IS CLAIMED IS:**

1. A reflective liquid crystal display device comprising:

a plurality of gate lines and data lines intersecting on a first substrate, the gate line

5 and the data line defining pixel areas;

a thin film transistor formed at the intersection of the gate line and the data line, the thin film transistor including a gate electrode, a semiconductor layer, a source electrode and a drain electrode;

a capacitor lower electrode of a storage capacitor formed on the same plane as the gate line;

a capacitor upper electrode formed integrally with the drain electrode on the capacitor lower electrode;

a first insulation film inserted between the capacitor upper electrode and the capacitor lower electrode; and

15 a thin film transistor array substrate connected with the drain electrode and including the reflective electrode formed at the pixel area.

2. The reflective liquid crystal display as claimed in claim 1, wherein the first insulation film is one of silicone nitride(SiNx) and silicone oxide(SiOx).

3. The reflective liquid crystal display as claimed in claim 1, further comprising a second insulation film between the capacitor upper electrode and the reflective electrode.

4. The reflective liquid crystal display as claimed in claim 3, wherein the second insulation film is one of silicone nitride (SiNx), BCB and acryl resin.

5 5. A transflective liquid crystal display device, which has pixel areas defined into a reflection part and a transmission part, the liquid crystal display device comprising:

a plurality of gate lines and data lines intersecting on a first substrate, the gate line and the data line defining pixel areas;

10 a thin film transistor formed at the intersection of the gate line and the data line, the thin film transistor including a gate electrode, a semiconductor layer, a source electrode and a drain electrode;

a capacitor lower electrode of a storage capacitor formed on the same plane as the gate line;

15 a capacitor upper electrode formed integrally with the drain electrode on the capacitor lower electrode;

a first insulation film inserted between the capacitor upper electrode and the capacitor lower electrode;

a reflective electrode connected with the drain electrode and formed on the reflection area; and

20 a thin film transistor array substrate connected with the reflective electrode and including the transmissive electrode formed at the transmission area.

6. The transflective liquid crystal display device as claimed in claim 5, wherein the capacitor upper electrode extends along a boundary part between the reflective electrode and the transmissive electrode to prevent light leakage.

5 7. The transflective liquid crystal display device as claimed in claim 5, wherein the first insulation film is one of silicone nitride(SiNx) and silicone oxide(SiOx).

8. The transflective liquid crystal display device as claimed in claim 5, further comprising a second insulation film between the capacitor upper electrode and the reflective electrode.

9. The transflective liquid crystal display device as claimed in claim 8, wherein the second insulation film is one of silicone nitride (SiNx), BCB or acryl resin.

10. The transflective liquid crystal display device as claimed in claim 5, further comprising another first insulation film between the reflective electrode and the transmissive electrode.

11. A method for manufacturing a reflective liquid crystal display device, the method comprising:

intersecting a plurality of gate lines and data lines on a first substrate;

forming a thin film transistor on the intersection of the gate line and the data line, the

thin film transistor including a gate electrode, a semiconductor layer, a source electrode and a

drain electrode;

forming a capacitor lower electrode of a storage capacitor on the same plane as the gate line;

forming an insulation film on the capacitor lower electrode;

forming an capacitor upper electrode on an upper portion of the capacitor lower electrode, the capacitor upper electrode being formed integrally with the drain electrode; and

forming a reflective electrode connected with the drain electrode.

12. A method for manufacturing a transfective liquid crystal display device, which has pixel areas defined into a reflection part and a transmission part, the method comprising:

intersecting a plurality of gate lines and data lines on a first substrate;

forming a thin film transistor on the intersection of the gate line and the data line, the thin film transistor including a gate electrode, a semiconductor layer, a source electrode and a

drain electrode;

forming a capacitor lower electrode of a storage capacitor on the same plane as the gate line;

forming an insulation film on the capacitor lower electrode;

forming a capacitor upper electrode on an upper portion of the capacitor lower electrode, the capacitor upper electrode being formed integrally with the drain electrode;

forming a reflective electrode connected with the drain electrode at the reflection area; and

Parameter	Value	Unit
$\alpha_1$	0.0000	
$\alpha_2$	0.0000	
$\alpha_3$	0.0000	
$\alpha_4$	0.0000	
$\alpha_5$	0.0000	
$\alpha_6$	0.0000	
$\alpha_7$	0.0000	
$\alpha_8$	0.0000	
$\alpha_9$	0.0000	
$\alpha_{10}$	0.0000	
$\alpha_{11}$	0.0000	
$\alpha_{12}$	0.0000	
$\alpha_{13}$	0.0000	
$\alpha_{14}$	0.0000	
$\alpha_{15}$	0.0000	
$\alpha_{16}$	0.0000	
$\alpha_{17}$	0.0000	
$\alpha_{18}$	0.0000	
$\alpha_{19}$	0.0000	
$\alpha_{20}$	0.0000	
$\alpha_{21}$	0.0000	
$\alpha_{22}$	0.0000	
$\alpha_{23}$	0.0000	
$\alpha_{24}$	0.0000	
$\alpha_{25}$	0.0000	
$\alpha_{26}$	0.0000	
$\alpha_{27}$	0.0000	
$\alpha_{28}$	0.0000	
$\alpha_{29}$	0.0000	
$\alpha_{30}$	0.0000	
$\alpha_{31}$	0.0000	
$\alpha_{32}$	0.0000	
$\alpha_{33}$	0.0000	
$\alpha_{34}$	0.0000	
$\alpha_{35}$	0.0000	
$\alpha_{36}$	0.0000	
$\alpha_{37}$	0.0000	
$\alpha_{38}$	0.0000	
$\alpha_{39}$	0.0000	
$\alpha_{40}$	0.0000	
$\alpha_{41}$	0.0000	
$\alpha_{42}$	0.0000	
$\alpha_{43}$	0.0000	
$\alpha_{44}$	0.0000	
$\alpha_{45}$	0.0000	
$\alpha_{46}$	0.0000	
$\alpha_{47}$	0.0000	
$\alpha_{48}$	0.0000	
$\alpha_{49}$	0.0000	
$\alpha_{50}$	0.0000	
$\alpha_{51}$	0.0000	
$\alpha_{52}$	0.0000	
$\alpha_{53}$	0.0000	
$\alpha_{54}$	0.0000	
$\alpha_{55}$	0.0000	
$\alpha_{56}$	0.0000	
$\alpha_{57}$	0.0000	
$\alpha_{58}$	0.0000	
$\alpha_{59}$	0.0000	
$\alpha_{60}$	0.0000	
$\alpha_{61}$	0.0000	
$\alpha_{62}$	0.0000	
$\alpha_{63}$	0.0000	
$\alpha_{64}$	0.0000	
$\alpha_{65}$	0.0000	
$\alpha_{66}$	0.0000	
$\alpha_{67}$	0.0000	
$\alpha_{68}$	0.0000	
$\alpha_{69}$	0.0000	
$\alpha_{70}$	0.0000	
$\alpha_{71}$	0.0000	
$\alpha_{72}$	0.0000	
$\alpha_{73}$	0.0000	
$\alpha_{74}$	0.0000	
$\alpha_{75}$	0.0000	
$\alpha_{76}$	0.0000	
$\alpha_{77}$	0.0000	
$\alpha_{78}$	0.0000	
$\alpha_{79}$	0.0000	
$\alpha_{80}$	0.0000	
$\alpha_{81}$	0.0000	
$\alpha_{82}$	0.0000	
$\alpha_{83}$	0.0000	
$\alpha_{84}$	0.0000	
$\alpha_{85}$	0.0000	
$\alpha_{86}$	0.0000	
$\alpha_{87}$	0.0000	
$\alpha_{88}$	0.0000	
$\alpha_{89}$	0.0000	
$\alpha_{90}$	0.0000	
$\alpha_{91}$	0.0000	
$\alpha_{92}$	0.0000	
$\alpha_{93}$	0.0000	
$\alpha_{94}$	0.0000	
$\alpha_{95}$	0.0000	
$\alpha_{96}$	0.0000	
$\alpha_{97}$	0.0000	
$\alpha_{98}$	0.0000	
$\alpha_{99}$	0.0000	
$\alpha_{100}$	0.0000	